


## CV of Presenter

	Dr. Ken <u>Naono</u>	
Job Title:	Chief Researcher	
Organization:	Hitachi Asia (Malaysia) Sdn Bhd	
Major Field:	<p>He has been engaged in research and development of matrix libraries for the parallel computer HITACHI SR2201, SR8000, SR11000 and SR16000. He has been engaged also in research and development of auto-tuning and business monitoring system. He is now working as a visiting researcher at Universiti Teknologi PETRONAS for joint research on Green IT.</p>	
Education:	<p>Ph. D in Engineering, University of Electro-Communications, Tokyo, Japan, 2006.  MS in Mathematical Sciences, Kyoto University, Kyoto, Japan, 1994.  BS in Mathematics, Kyoto University, Kyoto, Japan, 1992.</p>	
Job history:	<p>He joined Central Research Laboratory, Hitachi, Ltd., Tokyo, Japan in 1994.  From 2005, Senior Researcher, Platform Systems Research Department, Central Research Laboratory, Hitachi, Ltd.  From 2011 to 2012; Deputy Manager of Platform Systems Research Department and Senior Researcher, Central Research Laboratory, Hitachi, Ltd.  He is currently Chief Researcher at R&amp;D department, Hitachi Asia Malaysia Sdn Bhd and Visiting Researcher at Universiti Teknologi PETRONAS.</p> <p>He received the 2000 Joint Symposium on Parallel Processing (JSPP) Best Paper Award, the 2006 Japan Society for Computational Engineering and Science (JSCES) Young Researcher Award and the 2009 Kanto Local Commendation for Invention. He is a member of IPSJ, JSIAM, JAFEE and JSCES.</p>	